



SHAHAJIRAO PATIL VIKAS PRATISHTHAN

S. B. PATIL COLLEGE OF ENGINEERING, INDAPUR, DIST: PUNE.

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ACA - R - 07	CLASS TIME TABLE	Academic Year: 2024-25
Rev : 01		Semester: I
Date: 12.06.2015		
Ref: SPVP/SBPCOE/E&TC/2024-25/F2/01		Date:04.07.2024
Class: BE		Wef: 15.07.2024
Name of Class Teacher:Prof.C.V.Nalawade		Class Room Location : 117/122

Time/ Day	9.00AM to 10.00AM	10:00AM-11:00AM	11.00AM to 12.0PM	12:00PM-1:00PM	1.00PM TO 2.00 PM	LONG BREAK (2:00 pm to 2:15 pm)	2.15PM to 3.15PM	3.15PM to 4.15PM	
MON	CC-B3(119A),RMT-B2(121)MIOT-B1(120)		SHORT BREAK 10.00AM TO 10.10AM	MIOT(117)	CC(117)		RMT(117)	T&P SESSION	
TUE	MIOT-B2 (120),VLSI-B1(119),RMT-B3(121)			MIOT(117)	VLSI(117)		CC(117)	MUSIC/NPTEL	
WED	VLSI-B3(119),CC-B2(119A),,RMT-B1(121)			EPD(117)	CC(117)		MIOT(117)	NPTEL	
THU	CC-B1(119A),MIOT-B3(120),VLSI-B2(119)			RMT(117)	EPD(117)		VLSI(117)	SPORT	
FRI	EPD(122)	VLSI(122)		PROJECT STAGE-I(123)			RMT(122/118)	PROJECT STAGE-I	
SAT	Audit Course-7(122)	PROJECT STAGE-I		PROJECT STAGE-I	TUTORMEET(119)		PROJECT STAGE-I(123)		

ABBR:	Name of Subject	Subject Incharge	ABBR:	Name of Subject	Subject Incharge
IOT	MODERNIZED INTERNET OF THINGS(TH+PR)(Elective-III)	Prof.S.M.Borate	EPD(Elective-IV)	ELECTRONICS PRODUCT DESIGN(TH)	Prof.A.S.Shirkande
RMT	RADIATION & MICROWAVE (TECHNIQUES(TH+PR)	Prof.C.V. Nalawade	(CC)	CLOUD COMPUTING (TH+PR)	Prof.V.T.Shejwal
VLSI	VLSI DESIGN & TECHNOLOGY (TH+PR)	Prof.V.U.Bansude	AC-7	Audit Course-7	Prof.V.U.Bansude


(Prof. P. S. Togrikar)

I/C Time Table


(Prof. V. U. Bansude)

Head of the Department
HEAD
Department of E & TC Engineering


(Prof. V. S. Nalawade)

Dean of Academic
Dean Academic
S.B. Patil College of Engineering
Indapur, Dist. Pune


(Dr. S. T. Shirkande)

Principal
Principal
S.B. Patil College of Engineering
Indapur, Dist. Pune