



SHAHAJIRAO PATIL VIKAS PRATISHTHAN
S. B. PATIL COLLEGE OF ENGINEERING, INDAPUR, DIST: PUNE.

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ACA - R - 07	CLASS TIME TABLE	Academic Year: 2024-25
Rev : 01		Semester: I
Date: 12.06.2015		

Ref: SPVP/SBPCOE/E&TC/2024-25/F2/01		Date:04.07.2024
Class: SE		Wef: 15.07.2024
Name of Class Teacher :Prof. A.S.Shirkande		Class Room Location : 118

Time/ Day	9.00AM to 10.00AM	10:00AM-11:00AM	11.00AM to 12.00PM	12:00PM-1:00PM	1.00PM TO 2.00 PM		2.15PM to 3.15PM	3.15PM to 4.15PM
MON	DC(118)	EXC(118)	SHORT BREAK 10.00AM TO 10.10AM	DS(118)	LIBRARY	LONG BREAK (2:00 pm to 2:15 pm)	EXC-S1(120A),DS-S2(119),ESD-S3(120)	
TUE	ELC(118)	M-III(118)		EXC(118)	DS(118)		DC-S1(123A),ELC-S2(121),DS-S3(119)	
WED	ELC(118)	M-III(118)		DC(118)	DS(118)		ESD-S1(120),EXC-S2(120A),DC-S3(123A)	
THU	M-III(118)	EXC(118)		ELC(118)	Digital Circuits(118)		ELC-S1(121),ESD-S2(120),EXC-S3(120A)	
FRI	DS(118)	M-III(118)		ELC(118)	NPTEL		DS-S1(119),DC-S2(123A),ELC-S3(121)	
SAT	M-III (TUT)(118)	EXC(118)		DC(118)	TUTORMEET(118)		Audit Course 3(118)	LIBRARY

ABBR:	Name of Subject	Subject Incharge	ABBR:	Name of Subject	Subject Incharge
EM-III	Engineering Mathematics-III(TH+TUT)	Prof.Y.P.Yadav	DC	Digital Circuits(TH+PR)	Prof.V.U.Bansude & Prof.A.N.Kawade
EXC	Electronic Circuits(TH+PR)	Prof.A.S.Shirkande	ESD	Electronic Skill Development(TW)	Prof.R.A.Sawant
ELC	Electrical Circuits(TH+PR)	Prof.R.R.Bairagi	Audit Course3	Audit Course3	Prof.C.V.Nalawade
DS	Data Structures (TH+PR)	Prof.C.V.Nalawade + Dr.S.M.karve			


(Prof. P. S. Togrikar)

I/C Time Table


(Prof. V. U. Bansude)

Head of the Department
HEAD

Department of E & TC Engineering
S.B. Patil College of Engineering, Indapur, Pune.


(Prof. V. S. Nalawade)

Dean of Academic
Dean Academic
S.B. Patil College of Engineering
Vangali, Tal. Indapur, Dist. Pune


(Dr. S. T. Shirkande)

Principal
Principal
S.B. Patil College of Engineering
Vangali, Tal. Indapur, Dist. Pune