



SHAHAJIRAO PATIL VIKAS PRATISHTHAN

S. B. PATIL COLLEGE OF ENGINEERING, INDAPUR, DIST: PUNE.

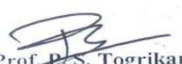
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ACA - R - 07	CLASS TIME TABLE	Academic Year: 2024-25
Rev : 01		Semester: II
Date: 12.06.2015		

Ref: SPVP/SBPCOE/E&TC/2024-25/F2/01		Date:06.01.2025
Class: SE		Wef: 13.01.2025
Name of Class Teacher :Prof. S.M.Borate		Class Room Location : 118

Time/ Day	9.00AM to 10.00AM	10:00AM-11:00AM	SHORT BREAK 10.00AM TO 10.10AM	12:00PM-1:00PM	1.00 PM to 2.0PM	SHORT BREAK (2:00 pm to 2:15 pm)	2.15PM to 3.15PM	3.15PM to 4.15PM
MON	CS(118)	POCS(118)		OOP(118)	SS(118)		ESD-S1(123A),OOPS-S2 (120),POCS-S3(121)	
TUE	OOP(118)	SS(118)		POCS(118)	NPTEL		ESD-S3(123A)POCS-S2(121),,S&CS-S1(119A)	
WED	SS(118)	CS(118)		POCS(118)	OOPS(118)		ESD-S2(123A),DA-S3(119),POCS-S1(121)	
THU	POCS(118)	ESD(118)		CS(118)	LIBRARY		OOPS-S1(120),,DA-S2(119),S&CS-S3(119A)	
FRI	OOP(118)	CS(118)		PBL(118)			OOP-S3(120),DA-S1(119),S&CS-S2(119A)	
SAT	ESD(118)	Audit Course 3 (118)		SAS(TUT)(118)	TUTORMEET(118)		PBL(118)	

ABBR:	Name of Subject	Subject Incharge	ABBR:	Name of Subject	Subject Incharge
SS	Signals & Systems (TH+TUT)	Prof.A.N.Kawade	PBL	Digital Circuits(TH+PR)	Dr..A.S.Shirkande
CS	Control Systems (TH+PR)	Prof.S M Borate	ESD	Electronic Skill Deveplopmnet(TW)	Dr..A.S.Shirkande
OOPS	Electrical Circuits(TH+PR)	Prof.R.A.Sawant	Audit Course3	Audit Course3	Prof.R.A.Sawant
POCS	Data Structures (TH+PR)	Prof.P.V.Nimbalkar			


(Prof. P. S. Togrikar)

I/C Time Table


(Prof. V. U. Bansude)

Head of the Department
HEAD

Department of E & TC Engineering
S. B. Patil College of Engineering, Indapur.


(Prof. V. S. Nalawade)

Dean of Academic
Dean Academic
S.B. Patil College of Engineering
Vangali, Tal. Indapur, Dist. Pune


(Dr. S. T. Shirkande)

Principal
Principal
S.B. Patil College of Engineering
Vangali, Tal. Indapur, Dist. Pune